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# Transformerless DC/DC Converters for High Gain using Switched Inductor

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*Abstract*—Oil and electricity prices have been soaring as the supply of global energy is increasingly becoming deficient. The limited availability of energy sources have become an inevitable global problem. With global energy shortage, distributed energy sources such as photo-voltaic cells, fuel cells, wind energy, micro turbine generators, etc., are now widely promoted around the world. The renewable energy grid connected power system with photo-voltaic and fuel cells calls for high step-up and high efficiency DC/DC converters because the low voltage generated by these energy sources should be boosted to a high voltage for inverter to feed the AC utility. A transformerless DC - DC high step up switched inductor converters is used here, which combines the passive and active switched inductor unit , which has the advantages like high voltage conversion ratio, low voltage stress across the switches, low conduction loss on switches, and easy to control . Analysis and comparison of various switched inductor based converter is also done in this paper.

Index Terms— High Step-Up DC/DC Converter; Switched Inductor; Switched Capacitor; Standalone.

I. INTRODUCTION

Taking the cost and reliability issues into consideration, the voltage of the fuel cell stack is usually lower, it is required to be boosted to a high voltage typically 380 V DC for the DC/AC Inverter[21]. The traditional boost converter cannot meet this requirement, since to get the required voltage the converter need to be operated with duty ratio close to 1 and it induces high current ripple with low efficiency [21].

Various topologies have been developed to provide a high step-up without an extremely high duty ratio[21]. The isolated converters which include the half bridge and full bridge types, can adjust the voltage ratio by increasing the turns ratio of the transformers [11]. As the transformer losses are a function of switching frequency, the maximum operating frequency is limited [13]. This results in increased converter size making the converter bulky and expensive[13]. If the industrial application does not require for DC isolation, the use of a transformer would only increase the cost, the volume, and the losses [7]. Non-isolated high step-up converters are employed to achieve high efficiency and low cost, and can be generalized as the coupled inductor type and non-coupled inductor type [21]. A number of coupled inductor based high step-up converters have been developed, by increasing the turns ratio of the coupled inductor, which is similar to that in isolated converters, high voltage gain can be achieved [21].

The conventional boost and buck-boost converters may be the simplest topologies [9]. However, they are

*Grenze ID: 01.GIJET.3.2.5* © *Grenze Scientific Society, 2017*  limited in practical applications by the latch up condition and the degradation in the overall efficiency as the duty ratio D approaches unity [9]. Furthermore, the resulting voltage stress of the switches is high [9]. The extreme duty ratio not only induces very large current ripples but also increases conduction losses [9]. The extreme duty cycle may even cause malfunctions at high switching frequency due to the very short conduction time of the diode in step-up converters [7].

Various switched inductor and switched capacitor structures are present to extend the voltage gain [21]. By splitting the output capacitor of a basic boost converter, and combining the resulting capacitors with the main switch in the form of a switched capacitor circuit, a new step up structure is realized [4]. An output filter is added as usual in boost converters for getting a free ripple output [4]. Simple switched capacitor step-up structures showed that the dc gain depends on the number of capacitors of the switched capacitor circuit [3].

With the transition in series and parallel connection of the switched inductor, an inherent high voltage gain can be achieved [21]. The switched inductor based boost converter is then derived, but the voltage gain is still limited and hard to deal with high voltage ratio to meet the demands of the inverter; in addition, the switch voltage stress is also high [21]. Although more switched cells can be added to increase the voltage conversion ratio, the topology is very complex [21]. The modified boost type with switched inductor technique structure of converter is very simple [8]. Only one power stage is used in this converter [8]. However, this converter has two issues: 1) three power devices exist in the current flow path during the switch-on period, and two power devices exist in the current flow path during the switch-off period, and 2) the voltage stress on the active switch is equal to the output voltage [8].

## II. PASSIVE SWITCHEDINDUCTOR

Fig. 1 shows the circuit configuration of the Passive switched inductor, which consists of two inductors  $L_1$ ,  $L_2$  and three diodes  $D_1$ ,  $D_2$ ,  $D_3$ , when the switch is on, diodes  $D_1$  and  $D_2$  is forward biased and  $D_3$  is reverse biased, two inductors are connected in parallel; when switch is off, diodes  $D_1$  and  $D_2$  are reverse biased and  $D_3$  is forward biased, then the two inductors are series connected.



Fig. 1. Passive switched inductor [8]

## A. Analysis of Passive Switched Inductor

The operating modes can be divided into two modes, defined as modes 1 and 2.

1) Mode1: During this time interval, switch S is turned on. The equivalent circuit is shown in Fig. 2. Inductors are charged in parallel from the DC source. Thus, the voltages across inductors are given as,



Fig. 2.Equivalent circuits of P-SL unit when switch is ON

2) Mode 2: During this time interval, switch S is turned off. The equivalent circuit is shown in Fig.3. The DC source and inductors are series connected to transfer the energies to  $C_o$  and the load. Voltage across inductors can be given as ,

$$V_{L_1} = V_{L_2} = \frac{V_{in} - V_o}{2}$$
(4)

$$V_{D_1} = V_{D_2} = \underbrace{V_o - V_{in}}_{2}$$
(5)  
$$V_S = V_o$$
(6)

Applying Volt-Sec balance,

$$V_{in}DT_{s} + \frac{V_{in} - V_{o}}{2} (1 - D)T_{s} = 0$$
<sup>(7)</sup>

Voltage gain of CCM,

$$G_{CCM} = \frac{V_o}{V_{in}} = \frac{1 + D}{1 - D}$$

Fig. 3.Equivalent circuits of P-SLunit when switch is OFF



Fig. 4.Active switched inductor [8]

# III. ACTIVE SWITCHED INDUCTOR

Fig. 4 shows the circuit configuration of the Active switched inductor, which consists of two inductors  $L_1$ ,  $L_2$  and two switches  $S_1$ ,  $S_2$ ; when the switches are on, two inductors are connected in parallel; when switches are off, and the two inductors are series connected.

# A. Analysis of Active Switched Inductor

The operating modes can be divided into two modes, defined as modes 1 and 2.

1) Mode 1: During this time interval, switches are turned on. The equivalent circuit is shown in Fig. 5. Inductors are charged in parallel from the DC source. Thus, the voltages across inductors are given as,

$$V_{L1} = V_{L2} = V_{in} \tag{9}$$
$$V_{Do} = V_{O} \tag{10}$$



Fig. 5.Equivalent circuits of A-SL unit when switch is ON

2) Mode 2 : During this time interval, switches are turned off. The equivalent circuit is shown in Fig.6. The DC source and Inductors are series connected to transfer the energies to  $C_o$  and the load. Voltage across inductors can be given as,

$$V_{L_1} = V_{L_2} = \frac{V_{in} - V_o}{2} \tag{11}$$

$$V_{S_1} = V_{S_2} = \frac{V_{in} + V_o}{2} \tag{12}$$

Applying Volt-Sec balance,

$$V_{in}DT_{5} + \frac{V_{in} - V_{o}}{2} (1 - D)T_{5} = 0$$
(13)



Fig. 6.Equivalent circuits of A-SLunit when switch is OFF

Voltage gain of CCM,

$$G_{CCM} = \frac{V_o}{V_{in}} = \frac{1 + D}{1 - D}$$

# IV. HYBRID SWITCHEDINDUCTOR

A high voltage gain can be achieved by combining the P-SL and A-SL units. The inductors  $L_1$  and  $L_2$  in A-SL unit can be substituted with P-SL unit, then the proposed H-SLCs can be obtained, as shown in Fig. 7. The circuit c consists of four inductors  $L_{1a}$ , $L_{1b}$ ,  $L_{2a}$ ,  $L_{2b}$  and two switches  $S_1$ ,  $S_2$ ; when the switches are on, inductors are connected in parallel; when switches are off, the inductors are series connected.



Fig. 7. Hybrid switched inductor for AC load

# A. Analysis of Hybrid Switched Inductor

The operating modes can be divided into two modes, defined as modes 1 and 2. 1) Mode 1 :During this time interval, switches are turned on. The equivalent circuit is shown in Fig. 9. Inductors are charged in parallel from the DC source.



Fig. 8. Hybrid switched inductor [21]

Thus, the voltages across inductors are given as,

$$V_{L_{1a}} = V_{L_{2a}} = V_{L_{2b}} = V_{L_{1b}} = = V_{in}$$
(15)  
$$V_{D_{2c}} = V_{D_{1c}} = V_{in}$$
(16)  
$$V_{D} = V_{o} + V_{in}$$
(17)

2) Mode 2 : During this time interval, switch S is turned off. The equivalent circuit is shown in Fig.10. The DC source and inductors are series connected to transfer the energies to C and the load. Voltage across inductors can be given as,



Fig. 9.Equivalent circuits of H-SL unit when switch is ON

$$V_{D_{1s}} = V_{D_{1b}} = V_{D_{2s}} = V_{\frac{D_{2b}}{4}} = \frac{\frac{v_{in} - v_o}{4}}{2}$$
(19)  
$$V_{S_1} = V_{S_2} = \frac{v_{in} + v_o}{2}$$
(20)

Applying Volt-Sec balance,

$$V_{in}DT_{s} + \frac{V_{in} - V_{a}}{4} (1 - D)T_{s} = 0$$
(21)

Voltage gain of CCM,



Fig. 10.Equivalent circuits of H-SLunit when switch is OFF

# V. COMPARISON OF SWITCHED INDUCTOR CONVERTERS AND BOOST CONVERTER

The voltage stresses on the components (diode, switch) and the voltage gains of the converters are summarized in Table I. It can been seen that voltage stress on the active switch of the hybrid switched inductor and active switched inductor are less than that on the active switch of passive switched inductor and boost converter . Thus, the active switches with low voltage ratings and low ON-state resistance levels RDS(ON) can be selected. Moreover, the curves of the voltage gain of the boost converter and the switched inductor converters are shown Fig 11. As illustrated, the hybrid switched inductor converters can achieve high step-up voltage gain with low voltage stress.

	H-SLC	A-SLC	P-SLC	B-C
Voltage gain	$\frac{1+3D}{1-D}$	$\frac{1+D}{1-D}$	$\frac{1+D}{1-D}$	$\frac{1}{1-D}$
Voltage of MOSFET	$\frac{V n+}{VO}$	$\frac{V_{0}n+}{V_{0}}$	Vo	Vo
Voltage of Do	$V_{in} + V_O$	$V_{in} + V_O$	Vo	Vo

TABLE I: COMPARISON OF SWITCHED INDUCTOR CONVERTERS AND BOOST CONVERTER



Fig. 11. Voltage gain versus duty ratio of B-C and the three SL-C

## VI. SIMULATION RESULTS

Under the conditions  $V_{in} = 40V$ , inductors = 500  $\mu$ H;  $C_0 = 470\mu$ F, converters are simulated using the MATLAB in open loop at a switching frequency of 50KHz, duty cycle=0.74. Figure 12,13,14,15 represent the simulation results of boost converter (B-C), passive switched inductor converter (P-SLC), Active switched inductor converter (A-SLC), Hybrid switched inductor converter (H-SLC) respectively. It can been seen that H-SLC has low voltage stress and high voltage gain compared with other converters, hence closed loop simulation of H-SLC is done and the obtained dc output is given to the inverter to get acsupply.



Fig. 12. Waveform of Bo
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					103					
0.5										
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997 VOLTAGE ACROSS SW	0.1998 ITCH	0.1998	0.1998	0.1998	0.199
200										
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.199
40				vo	TAGE ACROSS DIODE L	/3				
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.199
				DK	DE VOLTAGE D1,02					
50									F	
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.199
100				VOLTA	OF ACROSS INDUCTOR					
100 0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.199
300				00	TPUT DIODE VOLTAGE					
<sup>®</sup> E										
0.1997 41	0.1997	0.1997	0.1997	0.1997	0.1997 NPUT VOLTAGE	0.1998	0.1998	0.1998	0.1998	0.199
39 0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.199
230					OUTPUT YOLTAGE					_
210										
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1996	0.1998	0.1998	0.199
\$					NDUCTOR CURRENT					
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.199

#### Fig. 13. Waveform of P-SLC

1										
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.199
				V	OLTAGE ACROSS SA	тсн				
100										
0,1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.195
					NDUCTOR VOLTAGE					
50										
-50									t	
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997 INDUCTOR CLIRREN	0.1998	0.1998	0.1998	0.1998	0.19
3										
-	1		i							
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.19
300					UTPUT DIODE VOLTA	DE				
100			·····						·····	
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.195
41					NPUT VOLTAGE					
40										
39	1	1	1	1	i	1	1	1	1	
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.19
210					OUTPUT VOLTAGE					
220	1	1		1	1	1		1		
200	1	i	1	i	i		i	1		
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.196

Fig. 14. Waveform of A-SLC

					VOS					
1										
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997 VOLTAGE ACROSS SWIT	0.1998 CH	0.1996	0.1998	0.1998	0.1
1997	0.1997	0.1997	0.1997	0.1997	0.1997 INDUCTOR VOLTAGE	0.1998	0.1998	0.1998	0.1998	0.1
1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.1
4				_				<hr/>		-
3 1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1996	0.1998	0.1998	0.11
E				VOLATOE	ACROSS DIODE D1,D2					
1997	0.1997	0.1997	0.1997	0.1997	0.1997 VOLTAGE ACROSS DIODE	0.1998 DC	0.1998	0.1998	0.1998	0.19
8 = · · · · · · · · · · · · · · · · · ·										
1997	0.1997	0.1997	0.1997	0.1997 VOLTAG	ACROSS OUTPUT DIODE	0.1998	0.1998	0.1998	0.1998	0.11
0										
1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.1
1	1		1		Here I gained	1	1	1		
°	i	1	i	1	i	1	1	1	1	
1997	0.1997	0.1997	0.1997	0.1997	CUTPUT VOLTAGE	0.1998	0.1998	0.1998	0.1990	0.1
8										
0.1997	0.1997	0.1997	0.1997	0.1997	0.1997	0.1998	0.1998	0.1998	0.1998	0.1

Fig. 15. Waveform of H-SLC



Fig. 16.Simulation Diagram



Fig. 17. Waveform (closed loop ) of H-SLC

## VII. CONCLUSION

The SLC has been designed and simulated using MATLAB software. Based on the results obtained it can be concluded that:

- 1 The H-SLC can achieve a high gain with a small duty cycle that is difficult for B-C, P-SLC, A-SLC converter.
- 2 The proposed H-SLC combines the advantages of a passive switched inductor, active switched inductor and boost converter which can bring down the voltage stress of switches

#### References

- R. D. Middlebrook, "Transformerless Dc-to-dc Converters With Large Conversion Ratios," IEEE Trans. Power electron., Vol. 3, no. 4, pp.484 - 488, oct. 1988.
- [2] Q. Zhao and F. C. Lee," High-efficiency, high step-up DCDC converters," IEEE Trans. Power Electron., vol. 18, no. 1, pp. 65 - 73, Jan. 2003.
- [3] O. Abutbul, A. Gherlitz, Y. Berkovich, and A. Ioinovici, "Step-up switching-mode converter with high voltage gain using a switched ca- pacitor circuit," IEEE Trans. Circuits Syst.I, Fundam. Theory Appl., vol. 50, no. 8, pp. 1098 -1102, Aug. 2003
- [4] B. Axelrod, Y. Berkovich, and A. Ioinovici, "Transformerless dc dc converters with a very high dc line-to-load voltage ratio," J. Circuits, Syst. Comput., vol. 13, no. 3, pp. 467 - 475, Jun. 2004.
- [5] Wair.J., Duanr.Y , "High-efficiency DC/DC converter with high voltage gain", IEE Proc. Electr. Power Appl., 2005, 152,(4), pp. 793 802
- [6] B. R. Lin and J. J. Chen, "Analysis and implementation of a soft switch- ing converter with high-voltage conversion ratio,"IET Power Electron., vol. 1, no. 3, pp. 386 - 394, Sep. 2008
- [7] Y. Axelrod Berkovich and A. Ioinovici, "Switched-capacitor/ switched- inductor structures for getting transformerless hybrid DCDC PWM converters," IEEE Trans. Circuits Syst. I, Reg. Papers, vol. 55, no. 2, pp. 687 - 696, Mar. 2008
- [8] L. S. Yang, T. J. Liang, and J. F. Chen, "DC-DC converters with high step-up voltage gain," IEEE Trans. Ind. Electron, vol. 56, no. 8, pp. 3144 - 3152, Aug. 2009
- [9] C. T. Pan and C. M. Lai, "High-efficiency high step-up converter with low switch voltage stress for fuel-cell system applications," IEEE Trans. Ind.Electron.,vol.57,no.6, pp.1998-2006, Jun. 2010.
- [10] Wuhua Li, Yi Zhao, Yan Deng ,"Converter with Voltage Multiplier efficiency Conversion", IEEE Trans. Power Electron., Vol. 25, No. 9, September 2010.
- [11] L. S. Yang and T. J. Liang, "Analysis and implementation of a novel bidirectional DC-DC converter," IEEE Trans. Ind. Electron., vol. 59, no. 1, pp. 422 - 434, Jan. 2012
- [12] Y. P. Hsieh, J. F. Chen, T. J. Liang, and L. S. Yang, "Novel high step-up DC-DC converter with coupledinductor and switched-capacitor techniques," IEEE Trans. Ind. Electron., vol. 59, no. 2, pp. 998 - 1007, Feb. 2012.
- [13] R. G. GanesanandM. Prabhakar, "Non-isolated high gain boost con- verter for photovoltaic applications," IEEE in Proc. ICPEC ,2013, pp. 277 - 280.
- [14] P. H. Tseng, J. F. Chen, and Y. P. Hsieh, "A novel active clamp high step up DC-DC converter with coupledinductor for fuel cell system," in Proc. IEEE IFEEC, 2013, pp. 326 - 331.
- [15] K. C. Tseng, J. T. Lin, and C. A. Cheng, "An integrated derived boost flyback converter for fuel cell hybrid electric vehicles," IEEE IFEEC in Proc., 2013, pp. 283 - 287
- [16] J. H. Lee, T. J. Liang, and J. F. Chen, "Isolated coupled-inductor integratedDC-DCconverterwithnondissipativesnubberforsolarenergy applications," IEEE Trans. Ind. Electron., vol. 61, no. 7, pp. 3337-3348, Jul. 2014.
- [17] T HariPriya ; Alivelu M. Pa rimi ; U. M. Rao "Evaluation of High Voltage Gain Boost Converters for DC Grid Integration",2016 International Conference on Circuit, Power and ComputingTechnologies [ICCPCT], 2016, Pages:1 -6.
- [18] Chih-Lung Shen ., Po-Chieh Chiu , "Buck-boost-flyback integrated converter with single switch to achieve high voltage gain for PV or fuel- cell applications", Power Electronics 2016, Volume: 9, Issue: 6, Pages: 1228 - 1237
- [19] Sri Revathi B.;MahalingamPrabhakar, "Transformerless high-gain DC DC converter for microgrids" IET Power Electronics 2016, Volume: 9, Issue: 6 Pages: 1170 1179
- [20] B Axelrod; Y. Berkovich; A. Ioinovici, "Hybrid Switched Capacitor uk / Zeta / Sepic Converters In Step-Up Mode", IEEE International SymposiumonCircuitsandSystems, 2005Pages: 1310-1313, Vol.2
- [21] Yu Tang; DongjinFu; Ting WAng, "Hybrid Switched Inductor for High Step- Up Conversion", IEEE Trans. Ind. Electron., 2015 Pages: 1480 - 1490, Vol.62